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**EE4620L/EE6620L/CEG4324L/CEG6324L**

**DIGITAL INTEGRATED CIRCUIT DESIGN LAB**

**Lab x**

By

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Submission date: xx/xx/2024

“I have neither given nor received aid on this assignment, nor have I observed any violation of the Honor code”

Signature : Alex Yeoh

Date : xx/xx/2024

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